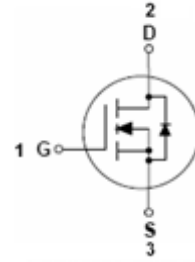


Features

- Low power loss by high speed switching and low on-resistance
- Excellent thermal behavior
- HBM: JESD22-A114-B: 1B
- Product validation acc. JEDEC Standard
- RoHS compliant with Halogen-free

HF

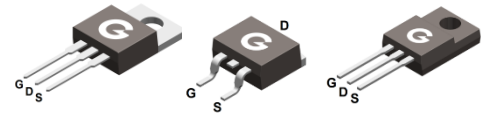


Applications

- PFC power supply stages
- Lighting applications
- Adapter

Mechanical Data

- Case: TO-220AB, TO-263, ITO-220AB
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208



TO-220AB TO-263 ITO-220AB

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
SJM60R280	TO-220AB	50 pcs / Tube	SJM60R280
SJM60R280B	TO-263	50 pcs / Tube or 800 pcs / Tape & Reel	SJM60R280B
SJM60R280F	ITO-220AB	50 pcs / Tube	SJM60R280F

Maximum Ratings (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	600	V
Gate-to-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	15	A
Continuous Drain Current ($T_C = 100^\circ\text{C}$)		9.5	A
Pulsed Drain Current ($t_p = 10\mu\text{s}$, $T_C = 25^\circ\text{C}$)	I_{DM}	60	A
Single Pulse Avalanche Energy ^{*2}	E_{AS}	300	mJ
Power Dissipation (TO-220AB, $T_C = 25^\circ\text{C}$)	P_D	104	W
Power Dissipation (TO-263, $T_C = 25^\circ\text{C}$)		104	W
Power Dissipation (ITO-220AB, $T_C = 25^\circ\text{C}$)		35	W
Operating Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case (TO-220AB, TO-263)	R _{θJC}	-	0.9	1.2	°C/W
Thermal Resistance Junction-to-Case (ITO-220AB)		-	3.3	3.6	°C/W
Thermal Resistance Junction-to-Air (TO-220AB, TO-263)	R _{θJA}	-	-	62	°C/W
Thermal Resistance Junction-to-Air (ITO-220AB)		-	-	75	°C/W

Electrical Characteristics (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	600	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±30V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *1	V _{GS} = 10V, I _D = 6A	-	250	280	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.5	3.7	4.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	7.6	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V	-	823	-	pF
C _{OSS}	Output Capacitance	V _{DS} = 40V	-	67	-	
C _{RSS}	Reverse Transfer Capacitance	f = 250kHz	-	0.7	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time *3	V _{DD} = 400V	-	11	-	ns
t _r	Turn-on Rise Time *3	V _{GS} = 10V	-	10	-	
t _{d(OFF)}	Turn-Off Delay Time *3	R _G = 10Ω	-	49	-	
t _f	Turn-Off Fall Time *3	I _D = 5.3A	-	10	-	
Q _G	Total Gate-Charge	V _{DD} = 520V	-	22.5	-	nC
Q _{GS}	Gate to Source Charge	V _{GS} = 10V	-	4.6	-	
Q _{GD}	Gate to Drain (Miller) Charge	I _D = 8A	-	12.8	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *1	I _{SD} = 6A, V _{GS} = 0V	-	0.8	1.2	V
t _{rr}	Reverse Recovery Time	I _F = 6A, V _R = 400V	-	270	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt = 100 A/μs	-	2.8	-	μC

Notes:

- The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
- The E_{AS} data shows Max. rating. The test condition is V_{DD} = 100V, V_{GS} = 15V, L = 50mH
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

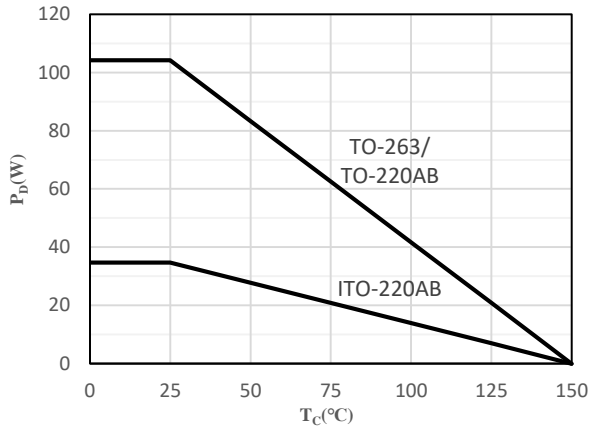


Fig 1 Power Dissipation

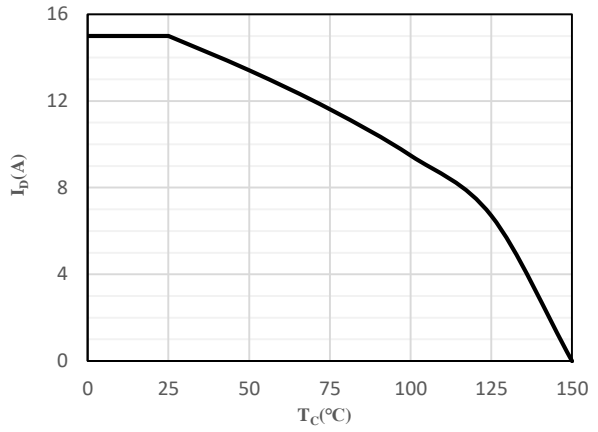


Fig 2 Drain Current

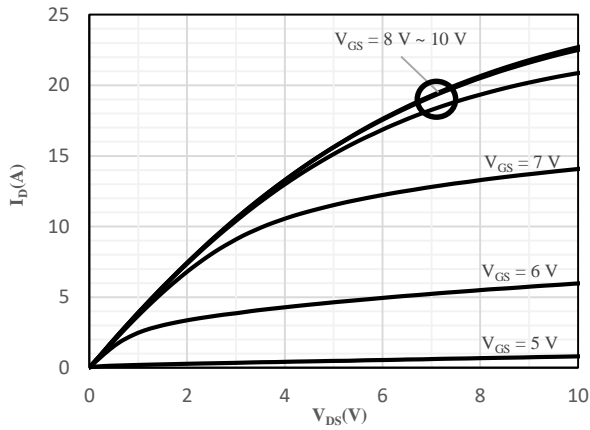


Fig 3 Typical Output Characteristics

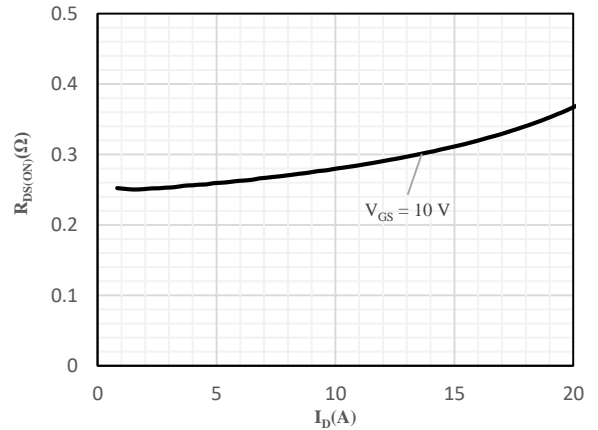


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

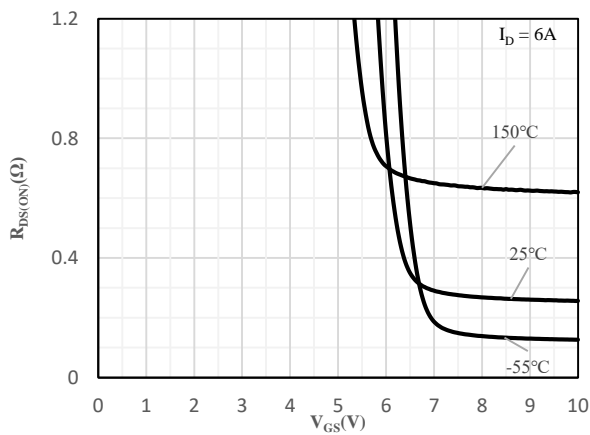


Fig 5 On-Resistance vs. Gate-Source Voltage

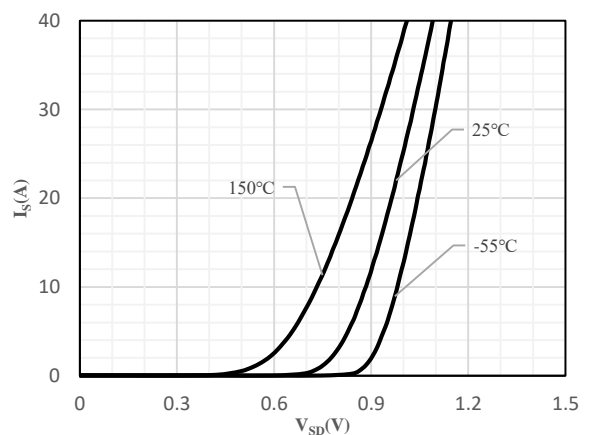


Fig 6 Body-Diode Characteristics

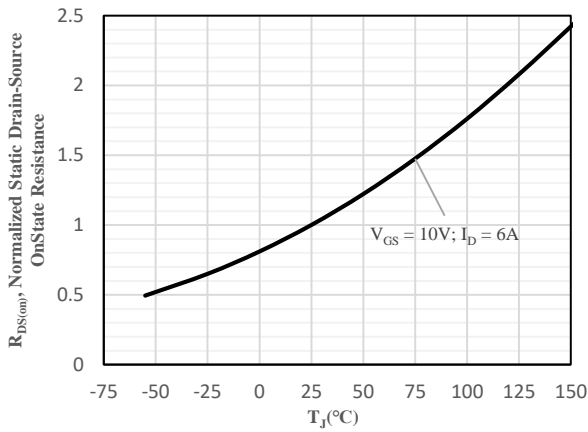


Fig 7 Normalized On-Resistance vs. Junction Temperature

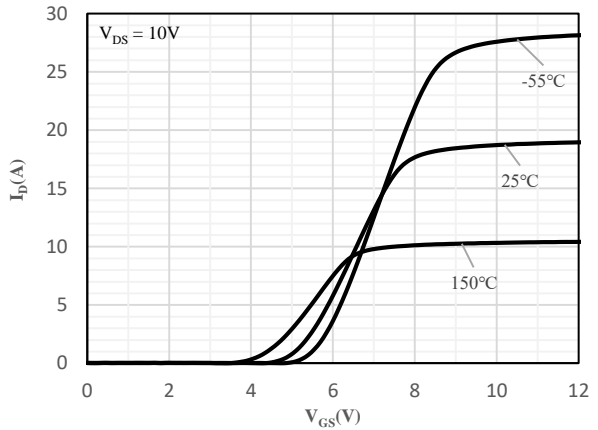


Fig 8 Transfer Characteristics

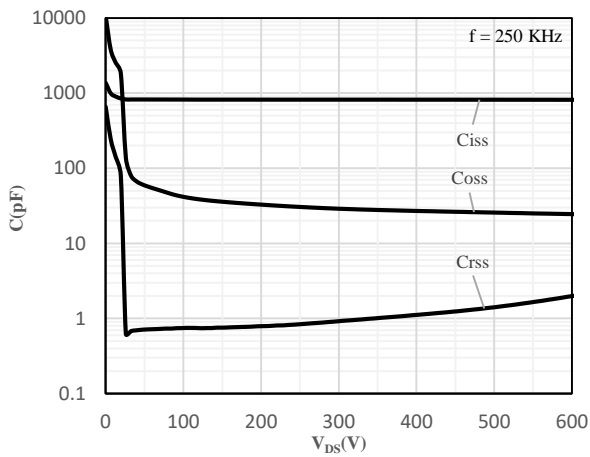


Fig 9 Capacitance Characteristics

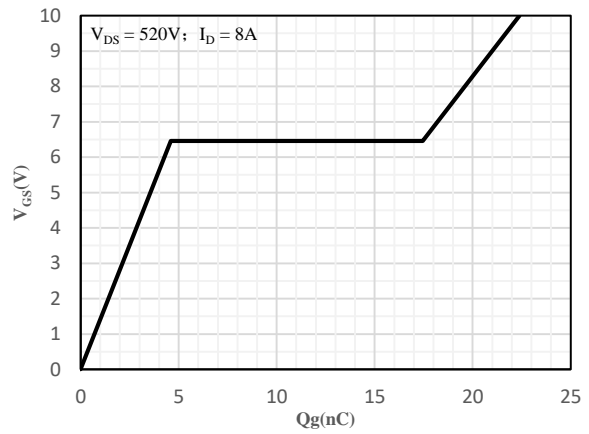


Fig 10 Gate-Charge Characteristics

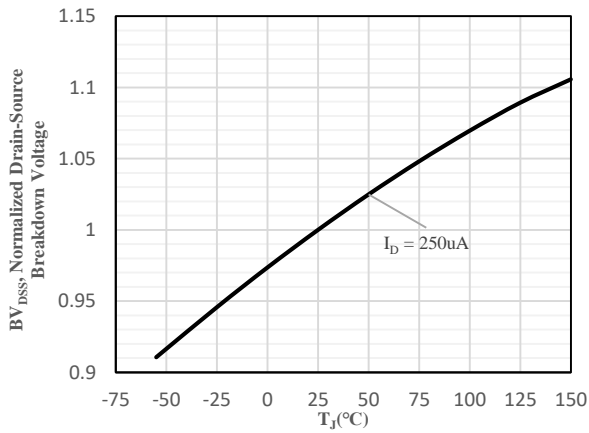


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

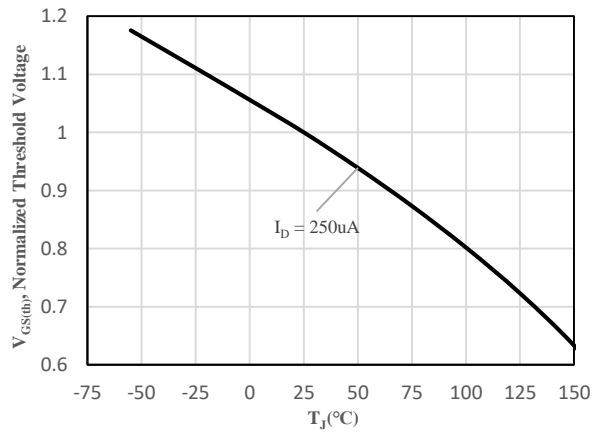


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

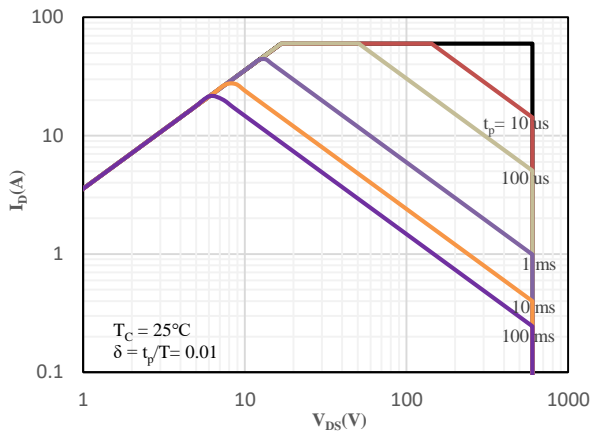


Fig 13 Safe Operating Area (TO-220AB / TO-263)

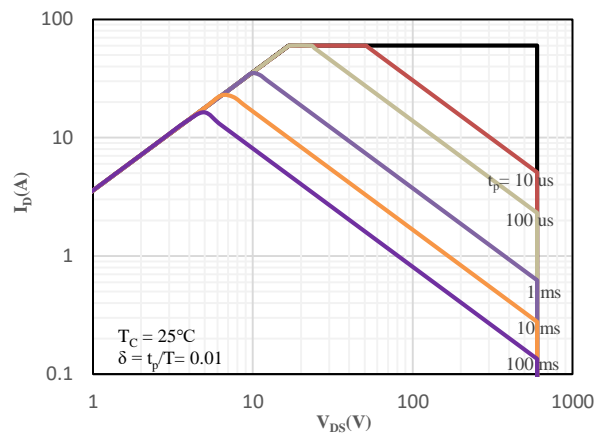


Fig 14 Safe Operating Area (ITO-220AB)

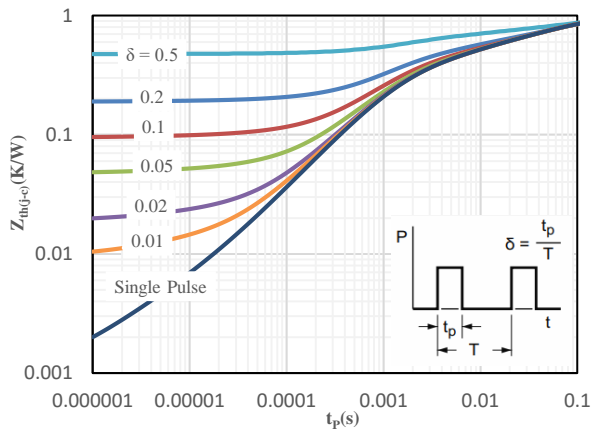


Fig 15 Maximum transient thermal impedance (TO-220AB / TO-263)

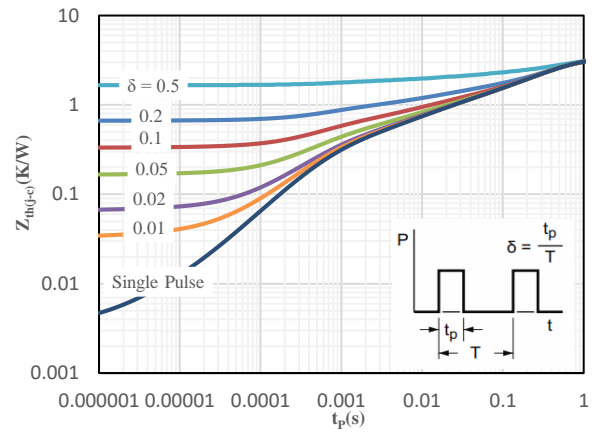
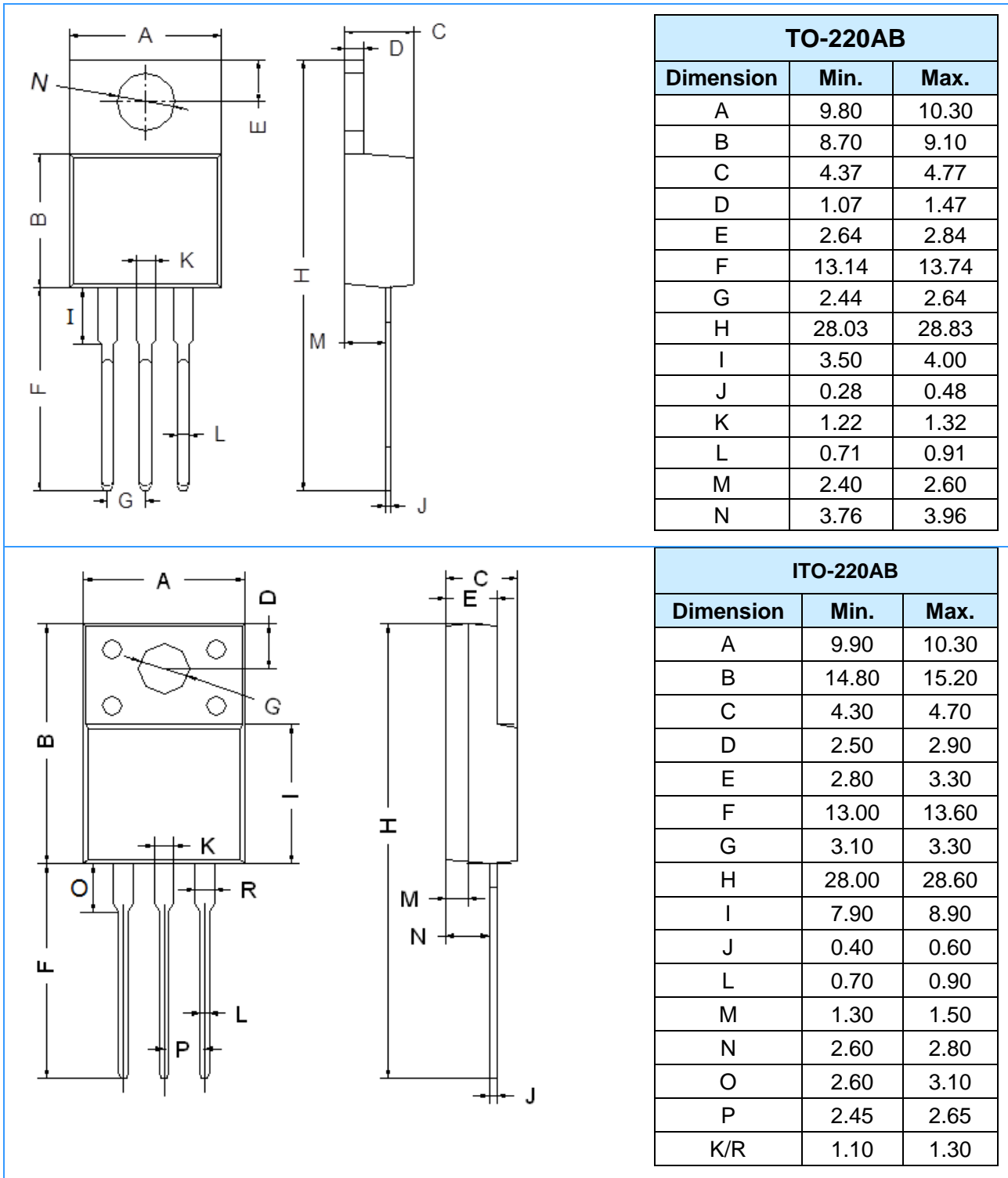
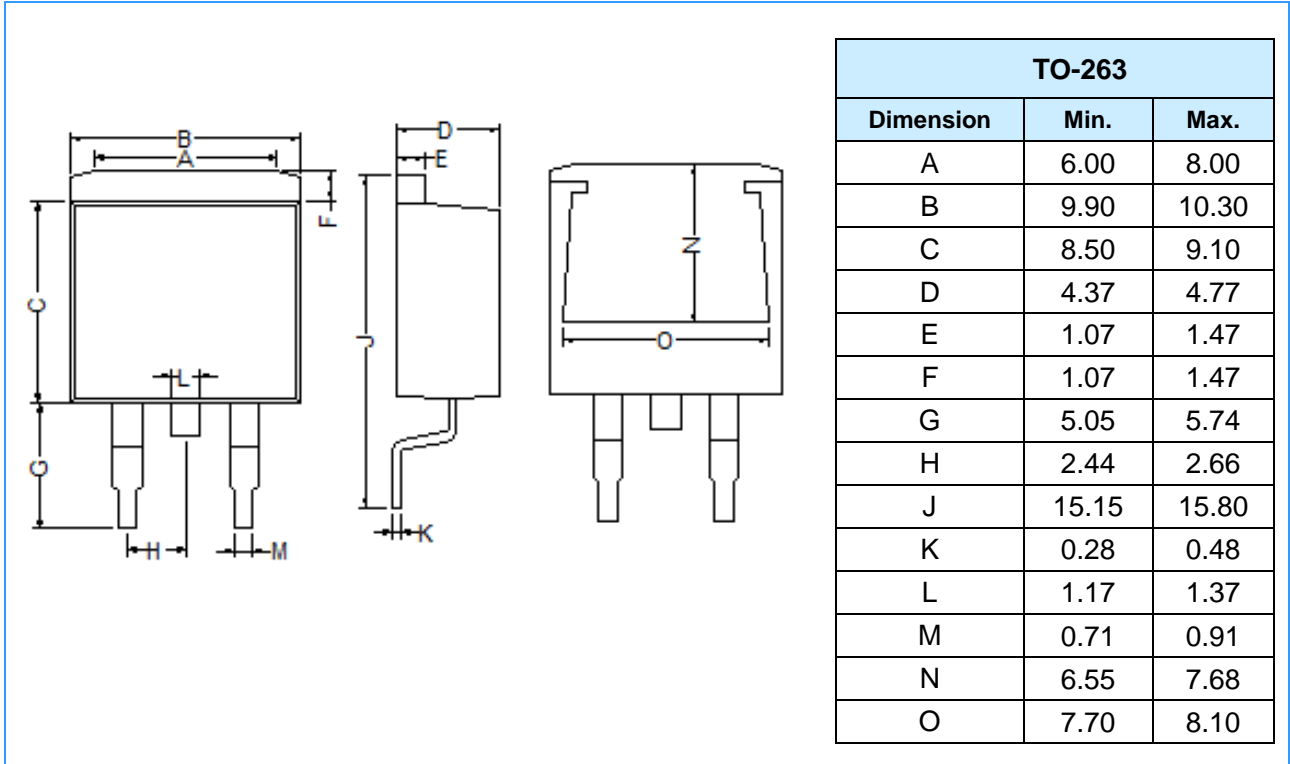


Fig 16 Maximum transient thermal impedance (ITO-220AB)

Package Outline Dimensions (Unit: mm)





Mounting Pad Layout (Unit: mm)

